IN THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (Original) A repair circuit comprising:

A bit fail repair block for receiving column and row addressees to determine whether the addresses are fail addresses in order to decide whether bit repair for the fail addresses are to be performed;

a row repair block for determining whether the row addresses are fail and deciding whether row repair for the row addresses are to be performed depending on the output of the bit fail repair block; and

a plurality of column repair blocks for deciding whether column repair for the column addresses are to be performed and deciding whether a normal column driver must be selected, depending on the column addresses, column fuse boxes and an output signal of the bit rail repair block.

2. (Original) The repair circuit as claimed in claim 1, wherein the bit fail repair block comprises:

A plurality of first fuse boxes for receiving the column and row addresses;

a first fuse summation block for summating the outputs of the plurality of the fuse boxes;

a normal word line driver that operates depending on the output of the fuse summation block to select a normal word line; and

a bit repair word line driver that operates depending on the output of the fuse summation block to repair one bit.

3. (Original) The repair circuit as claimed in claim 1, wherein the row repair block comprises:

A plurality of second fuse boxes for receiving the row addresses;

a second fuse summation block for summating the outputs of the plurality of the fuse boxes;

a normal word line driver for enabling the normal word line depending on the output of the first fuse summation block and the output of the second fuse summation block; and

a repair word line driver for enabling the normal word line depending on the output of the first fuse summation block and the output of the second fuse summation block.

4.   (Currently Amended) The repair circuit as claimed in claim 1, wherein each of the plurality of the column repair blocks comprises:

A column fuse box for receiving the column addresses:

a repair column select unit for selecting a column repair depending on the output of the column fuse box and the output of ~~the~~ a first fuse summation block;

a plurality of normal column select units for selecting a normal column depending on the column addresses, the outputs of the column fuse boxes and the output of the first fuse summation block;

a plurality of bit repair column select units for selecting a bit repair column depending on the output of the first fuse summation block and the output of the ~~first~~ column fuse box;

a repair column driver for selecting a repair column depending on the output of the repair column select unit; and

a normal column driver for selecting a normal column and a bit repair column depending on the outputs of the normal column select units and the repair column select units.

5.   (Original) The repair circuit as claimed in claim 2, wherein the first fuse summation block receives the outputs of the plurality of the first fuse boxes as inputs,

and wherein if one of the inputs has a value different from the other inputs, its output value is decided by a value different from the value of the one input.